

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,221,350 B2
APPLICATION NO. : 09/826096
DATED : May 22, 2007
INVENTOR(S) : Wu et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover page, Item (73) Assignee:

Please amend Assignee's name as follows from "Chi Mai Optoelectronics" to --CHI MEI OPTOELECTRONICS CORP.--

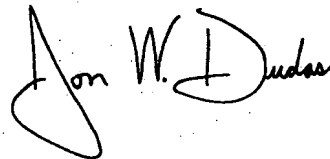
Column 6, Line 9 of Claim 6:

Please amend the Line 9 as follows from:

"gate voltage deformation means for, generating a" to --gate voltage deformation means for generating a--

Signed and Sealed this

Sixth Day of November, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a cursive "Dudas".

JON W. DUDAS

Director of the United States Patent and Trademark Office